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Practitioner's Docket No.: TRQ-00004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of (1st Inventor): Walter A. Wohlmuth
Assignee: TriQuint Semiconductor, Inc.
Serial No.: 10/721,437-7134 Group No.: 2818
Filed: 11/24/2003 Examiner: David C. Nelms
For: "Monolithic Integrated Enhancement Mode And Depletion Mode Field
Effect Transistors And Method Of Making The Same"

Date: August 26, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the outstanding Office Action dated July 29, 2005, in which the Examiner imposed a restriction requirement to election of invention for the above-referenced application, Applicant elects to prosecute the invention as in Group I, Claims 1-17 drawn to a semiconductor device(s), without traverse.

Applicant reserves the right to file divisional applications on the non-elected claims.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Aug. 26, 2005

Carrie Reddick
Signature: Carrie Reddick